Memory abstraction 1

MPRI — Cours 2.6 "Interprétation abstraite : application à la vérification et à l'analyse statique"

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Overview of the lecture

So far, we have shown numerical abstract domains

- non relational: intervals, congruences...
- relational: polyhedra, octagons, ellipsoids...
- How to deal with non purely numerical states ?
- How to reason about complex data-structures ?
- ⇒ a very broad topic, and two lectures:

This lecture

- overview memory models and memory properties
- abstraction of pointer structures and separation logic based shape analysis

Next lecture: arrays, shape/numerical abstraction, composition of shape abstractions

Outline

- Memory models
 - Towards memory properties
 - Formalizing concrete memory states
 - Treatment of errors
 - Language semantics
- 2 Pointer Abstractions
- Separation Logic
- 4 A shape abstract domain relying on separation
- Standard static analysis algorithms
- 6 Conclusion
- 7 Internships

Assumptions for the two lectures on memory abstraction

Imperative programs viewed as transition systems:

- set of control states: L (program points)
- set of variables: X (all assumed globals)
- set of values: V (so far: V consists of integers (or floats) only)
- set of memory states: \mathbb{M} (so far: $\mathbb{M} = \mathbb{X} \to \mathbb{V}$)
- error state: Ω
- states: S

$$\mathbb{S} = \mathbb{L} \times \mathbb{M}$$
 $\mathbb{S}_{\Omega} = \mathbb{S} \uplus \{\Omega\}$

• transition relation:

$$(
ightarrow)\subseteq \mathbb{S} imes rac{\mathbb{S}_\Omega}{}$$

Abstraction of sets of states

- abstract domain D[♯]
- concretization $\gamma: (\mathbb{D}^{\sharp}, \sqsubseteq^{\sharp}) \longrightarrow (\mathcal{P}(\mathbb{S}), \subseteq)$

Assumptions: syntax of programs

We start from the same language syntax and will extend l-values:

```
1 ::= I-values
pointers, array dereference...
e ::= expressions
s ::= statements
   \begin{array}{ll} & 1 = e & (assignment) \\ & s; \dots s; & (sequence) \\ & & \textbf{if}(e)\{s\} & (condition) \\ & & \textbf{while}(e)\{s\} & (loop) \end{array}
```

Assumptions: semantics of programs

We assume classical definitions for:

- I-values: $[1]: \mathbb{M} \to \mathbb{X}$ $[\times]_{\mathbb{M}} (m) = \times$
- expressions: $\llbracket \mathtt{e} \rrbracket : \mathbb{M} \to \mathbb{V}$
- programs and statements:
 - we assume a label before each statement
 - ► each statement defines a set of transitions (→)

In this course, we rely on the usual reachable states semantics

Reachable states semantics

The reachable states are computed as $[S]_{\mathcal{R}} = \mathsf{lfp}F$ where

$$F: \mathcal{P}(\mathbb{S}) \longrightarrow \mathcal{P}(\mathbb{S}) \\ X \longmapsto \mathbb{S}_{\mathcal{I}} \cup \{s \in \mathbb{S} \mid \exists s' \in X, \ s' \to s\}$$

and $\mathbb{S}_{\mathcal{T}}$ denotes the set of initial states.

Assumptions: general form of the abstraction

We assume an abstraction for sets of memory states:

- memory abstract domain $\mathbb{D}_{\text{mem}}^{\sharp}$
- concretization function $\gamma_{\mathrm{mem}}: \mathbb{D}^{\sharp}_{\mathrm{mem}} \to \mathcal{P}(\mathbb{M})$

Reachable states abstraction

We construct $\mathbb{D}^{\sharp} = \mathbb{L} \to \mathbb{D}_{\text{mem}}^{\sharp}$ and:

$$\gamma: \mathbb{D}^{\sharp} \longrightarrow \mathcal{P}(\mathbb{S})$$
 $X^{\sharp} \longmapsto \{(\ell, m) \in \mathbb{S} \mid m \in \gamma_{\text{mem}}(X^{\sharp}(\ell))\}$

The whole question is how do we choose $\mathbb{D}_{mem}^{\sharp}, \gamma_{mem}...$

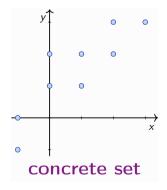
- previous lectures:
- \mathbb{X} is fixed and finite and, \mathbb{V} is scalars (integers or floats), thus, $\mathbb{M} \equiv \mathbb{V}^n$
- today: we will **extend the language** thus, also **need to extend** $\mathbb{D}_{\mathrm{mem}}^{\sharp}, \gamma_{\mathrm{mem}}$

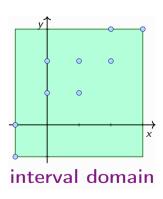
Abstraction of purely numeric memory states

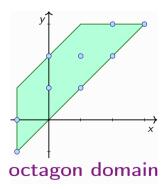
Purely numeric case

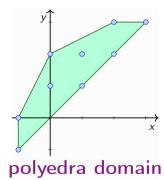
- V is a set of values of the same kind
- e.g., integers (\mathbb{Z}), machine integers ($\mathbb{Z} \cap [-2^{63}, 2^{63} 1]$)...
- If the set of variables is fixed, we can use any abstraction for \mathbb{V}^N

Example: N = 2, $X = \{x, y\}$









Heterogeneous memory states

In real life languages, there are many kinds of values:

- scalars (integers of various sizes, boolean, floating-point values)...
- pointers, arrays...

Heterogeneous memory states and non relational abstraction

- types t_0, t_1, \ldots and values $\mathbb{V} = \mathbb{V}_{t_0} \uplus \mathbb{V}_{t_1} \uplus \ldots$
- finitely many variables; each has a fixed type: $\mathbb{X} = \mathbb{X}_{t_0} \uplus \mathbb{X}_{t_1} \uplus \dots$
- memory states: $\mathbb{M} = \mathbb{X}_{t_0} \to \mathbb{V}_{t_0} \times \mathbb{X}_{t_1} \to \mathbb{V}_{t_1} \dots$

Principle: compose abstractions for sets of memory states of each type

Non relational abstraction of heterogeneous memory states

- $\mathbb{M} \equiv \mathbb{M}_0 \times \mathbb{M}_1 \times \dots$ where $\mathbb{M}_i = \mathbb{X}_i \to \mathbb{V}_i$
- Concretization function (case with two types)

$$egin{array}{lll} \gamma_{\mathrm{nr}} : & \mathcal{P}(\mathbb{M}_0) imes \mathcal{P}(\mathbb{M}_1) & \longrightarrow & \mathcal{P}(\mathbb{M}) \ & (extit{m_0^\sharp}, extit{m_1^\sharp}) & \longmapsto & \{(extit{m_0}, extit{m_1}) \mid orall i, \ extit{$m_i \in \gamma_i(extit{$m_i^\sharp$})$} \end{array}$$

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Memory structures

Common structures (non exhaustive list)

- Structures, records, tuples: sequences of cells accessed with fields
- Arrays: similar to structures; indexes are integers in [0, n-1]
- Pointers: numerical values corresponding to the address of a memory cell
- Strings and buffers: blocks with a sequence of elements and a terminating element (e.g., 0x0)
- Closures (functional languages):
 pointer to function code and (partial) list of arguments)

To describe memories, the definition $\mathbb{M} = \mathbb{X} \to \mathbb{V}$ is too restrictive

Generally, non relational, heterogeneous abstraction cannot handle many such structures all at once: relations are needed!

t Birt t.0

6.1

3

N E [0, 10]

Specific properties to verify

Memory safety

Absence of memory errors (crashes, or undefined behaviors)

Pointer errors:

Dereference of a null pointer / of an invalid pointer

Access errors:

Out of bounds array access, buffer overruns (often used for attacks)

Invariance properties

Data should not become corrupted (values or structures...)

Examples:

- Preservation of structures, e.g., lists should remain connected
- Preservation of invariants, e.g., of balanced trees

Properties to verify: examples

A program closing a list of file descriptors

Correctness properties

- memory safety
- 2 1 is supposed to store all file descriptors at all times will its structure be preserved? yes, no breakage of a next link
- closure of all the descriptors

Examples of structure preservation properties

- Algorithms manipulating trees, lists...
- Libraries of algorithms on balanced trees
- Not guaranteed by the language!
 e.g., the balancing of Maps in the OCaml standard library was incorrect for years (performance bug)

No one-to-one relation between memory cells and program variables

- a variable may indirectly reference several cells (structures...)
- dynamically allocated cells correspond to no variable at all...

Environment + Heap

- ullet Addresses are values: $\mathbb{V}_{\mathrm{addr}} \subseteq \mathbb{V}$
- ullet Environments $e \in \mathbb{E}$ map variables into their addresses
- Heaps $(h \in \mathbb{H})$ map addresses into values

$$\begin{array}{ccc} \mathbb{E} & = & \mathbb{X} \to \mathbb{V}_{\mathrm{addr}} \\ \mathbb{H} & = & \mathbb{V}_{\mathrm{addr}} \to \mathbb{V} \end{array}$$

h is actually only a partial function

• Memory states (or memories): $\mathbb{M} = \mathbb{E} \times \mathbb{H}$

Note: Avoid confusion between heap (function from addresses to values) and dynamic allocation space (often referred to as "heap")

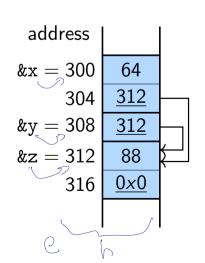
Example of a concrete memory state (variables)

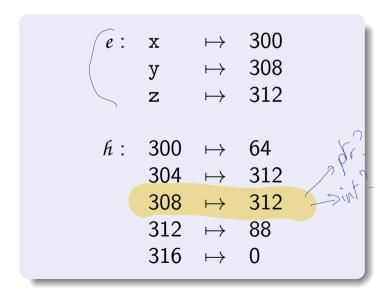
Example setup:

- x and z are two list elements containing values 64 and 88, and where the former points to the latter
- y stores a pointer to z

Memory layout

(pointer values underlined)



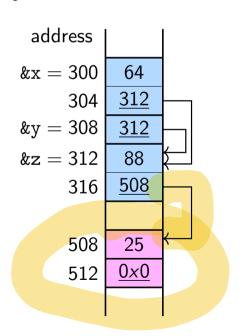


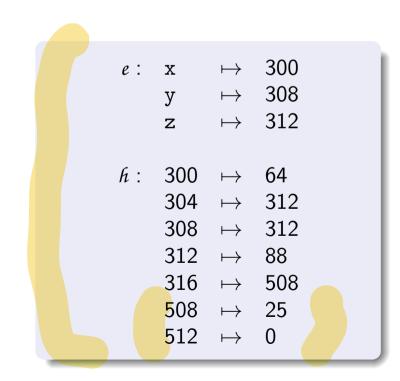
Example of a concrete memory state (variables + dyn. cell)

Example setup:

- same configuration
- + second field of z points to a dynamically allocated list element (in purple)

Memory layout





Extending the semantic domains

Some slight modifications to the semantics of the initial language:

- Addresses are values: $\mathbb{V}_{\mathrm{addr}} \subseteq \mathbb{V}$
- L-values evaluate into addresses: $[1]: \mathbb{M} \to \mathbb{V}_{addr}$

$$\llbracket x \rrbracket (e, h) = e(x)$$

• Semantics of expressions $[e]: \mathbb{M} \to \mathbb{V}$, mostly unchanged

$$[1](e,h) = h([1](e,h))$$

• Semantics of assignment $l_0 : 1 := e; l_1 : ...$

$$(l_0, e, h_0) \longrightarrow (l_1, e, h_1)$$

where

$$h_1 = h_0[[1](e, h_0) \leftarrow [e](e, h_0)$$

Realistic definitions of memory states

Our model is still not very accurate for most languages

- Memory cells do not all have the same size
- Memory management algorithms usually do not treat cells one by one, e.g., malloc returns a pointer to a block applying free to that pointer will dispose the whole block

Other refined models

- Partition of the memory in blocks with a base address and a size
- Partition of blocks into cells with a size
- Description of fields with an offset
- Description of pointer values with a base address and an offset...

For a **very formal** description of such concrete memory states: see **CompCert** project source files (Cog formalization)

Language semantics: program crash

In an abnormal situation, we assume that the program will crash

- advantage: very clear semantics
- disadvantage (for the compiler designer): dynamic checks are required

Error state

- Ω denotes an error configuration
- Ω is a **blocking**: $(\rightarrow) \subseteq \mathbb{S} \times (\{\Omega\} \uplus \mathbb{S})$

OCaml:

- out-of-bound array access:
 - Exception: Invalid_argument "index out of bounds".
- no notion of a null pointer

Java:

 exception in case of out-of-bound array access, null dereference: java.lang.ArrayIndexOutOfBoundsException

Language semantics: undefined behaviors

Alternate choice: leave the behavior of the program **unspecified** when an abnormal situation is encountered

- advantage: easy implementation (often architecture driven)
- disadvantage: unintuitive semantics, errors hard to reproduce different compilers may make different choices...
 or in fact, make no choice at all (= let the program evaluate even when performing invalid actions)

Modeling of undefined behavior

- Very hard to capture what a program operation may modify
- ullet Abnormal situation at (ℓ_0, m_0) such that $orall \mathit{m}_1 \in \mathbb{M}, \ (\ell_0, \mathit{m}_0) o (\ell_1, \mathit{m}_1)$
- In C:

array out-of-bound accesses and dangling pointer dereferences lead to undefined behavior (and potentially, memory corruption) whereas a null-pointer dereference always result into a crash

Composite objects

How are contiguous blocks of information organized?

Java objects, OCaml struct types

- sets of fields
- each field has a type
- no assumption on physical storage, no pointer arithmetics

C composite structures and unions

- physical mapping defined by the norm
- each field has a specified size and a specified alignment
- union types / casts:
 implementations may allow several views

Pointers and records / structures / objects

Many languages provide **pointers** or **references** and allow to manipulate **addresses**, but with different levels of expressiveness

What kind of objects can be referred to by a pointer?

Pointers only to records / structures / objects

- Java: only pointers to objects
- OCaml: only pointers to records, structures...

Pointers to fields

• C: pointers to any valid cell...

struct {int a; int b} x;

 $int * y = \&(x \cdot b);$

What kind of operations can be performed on a pointer?

Classical pointer operations

- Pointer dereference:
 - *p returns the contents of the cell of address p
- "Address of" operator: &x returns the address of variable x
- Can be analyzed with a rather coarse pointer model e.g., symbolic base + symbolic field

Arithmetics on pointers, requiring a more precise model

- Addition of a numeric constant:
 - p + n: address contained in p + n times the size of the type of p Interaction with pointer casts...
- Pointer subtraction: returns a numeric offset

Manual memory management

Allocation of unbounded memory space

- How are new memory blocks created by the program?
- How do old memory blocks get freed ?

OCaml memory management

- implicit allocation when declaring a new object
- garbage collection: purely automatic process, that frees unreachable blocks

C memory management

- manual allocation: malloc operation returns a pointer to a new block
- manual de-allocation: free operation (block base address)

Manual memory management is not safe:

- memory leaks: growing unreachable memory region; memory exhaustion
- dangling pointers if freeing a block that is still referred to

Summary on the memory model

Language dependent items

- Clear error cases or undefined behaviors for analysis, a semantics with clear error cases is preferable
- Composite objects: structure fully exposed or not
- Pointers to object fields: allowed or not
- Pointer arithmetic: allowed or not. i.e., are pointer values symbolic values or numeric values
- Memory management: automatic or manual

In this course, we start with a simple model, and study specific features one by one and in isolation from the others

Rest of the course

Abstraction for pointers and dynamic data-structures:

- pointer abstractions
- separation logic-based abstraction for dynamic structures
- three-valued logic-based abstraction for dynamic structures
- combination of value and structure abstractions

Abstract operations:

- post-condition for the **reading** of a cell defined by an I-value e.g., x = a[i] or x = *p
- post-condition for the writing of a heap cell
 e.g., a[i] = p or p -> f = x
- abstract join, that approximates unions of concrete states



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Programs with pointers: syntax

Syntax extension: we add pointer operations

```
1 ::= I-values
                          (x \in X)
        *e pointer dereference
1 · f field read
e ::= expressions
      1 [P] (e,h) = h([P] (e,h))
                          "address of" operator
   ::= statements
    x = malloc(c) allocation of c bytes
free(x) deallocation of the block pointed to by x
```

We do not consider pointer arithmetics here

Programs with pointers: semantics

Case of I-values:

Case of expressions:

Case of statements:

- memory allocation $\mathbf{x} = \mathbf{malloc}(c)$: $(e, h) \rightarrow (e, h')$ where $h' = h[e(\mathbf{x}) \leftarrow k] \uplus \{k \mapsto v_k, k+1 \mapsto v_{k+1}, \dots, k+c-1 \mapsto v_{k+c-1}\}$ and $k, \dots, k+c-1$ are fresh and unused in h
- memory deallocation free(x): $(e, h) \rightarrow (e, h')$ where k = e(x) and $h = h' \uplus \{k \mapsto v_k, k+1 \mapsto v_{k+1}, \dots, k+c-1 \mapsto v_{k+c-1}\}$

Pointer non relational abstractions

We rely on the **non relational abstraction of heterogeneous states** that was $\text{ we let } \mathbb{V} = \mathbb{V}_{addr} \uplus \mathbb{V}_{int} \text{ and } \mathbb{X} = \mathbb{X}_{addr} \uplus \mathbb{X}_{int}$

- concrete memory cells now include structure fields, and fields of dynamically allocated regions
- abstract cells C[♯] finitely summarize concrete cells
- we apply a **non relational abstraction**:

Non relational pointer abstraction

- ullet Set of pointer abstract values $\mathbb{D}_{\mathrm{ptr}}^{\sharp}$
- Concretization $\gamma_{\mathrm{ptr}}: \mathbb{D}_{\mathrm{ptr}}^{\sharp} \to \mathcal{P}(\mathbb{V}_{\mathrm{addr}})$ into pointer sets

We will see **several instances** of this kind of abstraction

Pointer non relational abstraction: null pointers

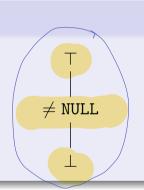
The dereference of a null pointer will cause a crash

To establish safety: compute which pointers may be null

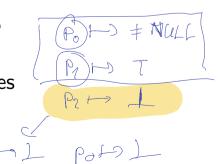
Null pointer analysis

Abstract domain for addresses:

- $\gamma_{\rm ptr}(\perp) = \emptyset$
- $\gamma_{\rm ptr}(\top) = \mathbb{V}_{\rm addr}$
- $ullet \ \gamma_{
 m ptr}(
 eq { t NULL}) = \mathbb{V}_{
 m addr} \setminus \{0\}$



- we may also use a lattice with a fourth element = NULL
 exercise: what do we gain using this lattice?
- very lightweight, can typically resolve rather trivial cases
- useful for C, but also for Java



Pointer non relational abstraction: dangling pointers

The dereferece of a null pointer will cause a crash

To establish safety: compute which pointers may be dangling

Null pointer analysis

Abstract domain for addresses:

- $\gamma_{\rm ptr}(\perp) = \emptyset$
- $\gamma_{\rm ptr}(\top) = \mathbb{V}_{\rm addr} \times \mathbb{H}$
- $\quad \gamma_{\rm ptr}({\sf Not \ dangling}) = \{(v, h) \mid h \in \mathbb{H} \land v \in {\sf Dom}(h)\}$



- very lightweight, can typically resolve rather trivial cases
- useful for C, useless for Java (initialization requirement + GC)

Pointer non relational abstraction: points-to sets

Determine where a pointer may store a reference to

```
1: int x, y;

2: int * p;

3: y = 9;

4: p = &x;

5: *p = 0;
```

- what is the final value for x?
 0, since it is modified at line 5...
- what is the final value for y?
 9, since it is not modified at line 5...

Basic pointer abstraction

• We assume a set of **abstract memory locations** A[#] is fixed:

$$\mathbb{A}^{\sharp} = \{\&x, \&y, \dots, \&t, a_0, a_1, \dots, a_N\} \quad \text{Nfl}$$

- Concrete addresses are abstracted into \mathbb{A}^{\sharp} by $(\phi_{\mathbb{A}}): \mathbb{A} \to \mathbb{A}^{\sharp} \uplus \{\top\}$
- A pointer value is abstracted by the abstraction of the addresses it may point to, i.e., $\mathbb{D}_{ptr}^{\sharp} = \mathcal{P}(\mathbb{A}^{\sharp})$ and $\gamma_{ptr}(a^{\sharp}) = \{a \in \mathbb{A} \mid \phi_{\mathbb{A}}(a) = a^{\sharp}\}$
- example: p may point to {&x}

Diff =
$$A^{\ddagger} - 3$$
 Diff
 $P \in Pomplir$
 $m = (e,h) \in \mathcal{S}(P)$
 iFE
 $\forall a^{\ddagger} \in A^{\ddagger}$
 $\forall a \in A$, $\phi_{A}(a) = a^{\ddagger}$
 $h(a) \in P$

Points-to sets computation example

Example code:

```
1: int x, y;

2: int * p;

3: y = 9;

4: p = &x;

5: *p = 0;

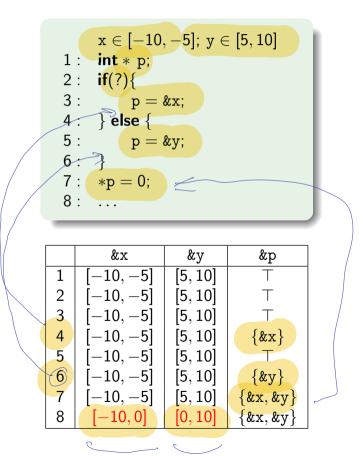
6: ...
```

Abstract locations: {&x, &y, &p}

Analysis results:

		&x	&y	&p
1	1			
	2	T	T	T
	3	T	T	T
	4	T	[9, 9]	T
	5	T	[9, 9]	{&x}
1	6	[0, 0]	[9, 9]	{&x}

Points-to sets computation and imprecision



- What is the final range for x?
- What is the final range for y?

Abstract locations: $\{&x,&y,&p\}$

Imprecise results

- The abstract information about both x and y are weakened
- The fact that $x \neq y$ is lost

Weak-updates

We can formalize this imprecision a bit more:

Weak updates

- The modified concrete cell cannot be uniquely mapped into a well identified abstract cell that describes only it
- The resulting abstract information is obtained by **joining the new value and**the old information

Effect in pointer analysis, in the case of an assignment:

- if the points-to set contains exactly one element, the analysis can perform a strong update as in the first example: $p \Rightarrow \{\&x\}$
- if the points-to set may contain more than one element, the analysis needs to perform a weak-update as in the second example: p \(\beta(\pix, \&\pi)\)

Pointer aliasing based on equivalence on access paths

Aliasing relation

Given m = (e, h), pointers p and q are aliases iff h(e(p)) = h(e(q))

Abstraction to infer pointer aliasing properties

• An access path describes a sequence of dereferences to resolve an I-value (i.e., an address); e.g.:

$$a := x \mid a \cdot f \mid *a$$

• An abstraction for aliasing is an over-approximation for equivalence relations over access paths

Examples of aliasing abstractions:

- set abstractions: map from access paths to their equivalence class (ex: $\{\{p_0, p_1, \&x\}, \{p_2, p_3\}, \ldots\}$)
- numerical relations, to describe aliasing among paths of the form $x(->n)^k$ (ex: $\{\{x(->n)^k, \&(x(->n)^{k+1}) \mid k \in \mathbb{N}\}\}$

Limitation of basic pointer analyses seen so far

Weak updates:

- imprecision in updates that spread out as soon as points-to set contain several elements
- impact client analyses severely (e.g., low precision on numerical)

Unsatisfactory abstraction of unbounded memory:

- ullet common assumption that \mathbb{C}^{\sharp} be finite
- programs using dynamic allocations often perform unbounded numbers of malloc calls (e.g., allocation of a list)

Unable to express well structural invariants:

- for instance, that a structure should be a list, a tree...
- very indirect abstraction in numeric / path equivalence abstration

A common solution: shape abstraction

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Separation logic principle: avoid weak updates

How to deal with weak updates?

Avoid them!

- Always materialize exactly the cell that needs be modified
- Can be very costly to achieve, and not always feasible
- Notion of property that holds over a memory region:
 special separating conjunction operator *
- Local reasoning:
 powerful principle, which allows to consider only part of the memory
- Separation logic has been used in many contexts, including manual verification, static analysis, etc...

Separation logic

Two kinds of formulas:

- **pure formulas** behave like formulas in first-order logic *i.e.*, are not attached to a memory region
- spatial formulas describe properties attached to a memory region

Pure formulas denote value properties

Pure formulas semantics: $\gamma(P) \subseteq \mathbb{E} \times \mathbb{M}$

Separation logic: points-to predicates

The next slides introduce the main separation logic formulas F ::= ...

We start with the most basic predicate, that describes a single cell:

Points-to predicate

• Predicate:

$$F ::= \dots | a \mapsto v |$$

where *a* is an address and *v* is a value

• Concretization:

$$(e,h) \in \gamma(1\mapsto v)$$
 if and only if $h=[[1](e,h)\mapsto v]$

• Example:

$$F = \&x \mapsto 18$$

ullet We also note 1 \mapsto e, as an I-value 1 denotes an address



Separation logic: separating conjunction

Merge of concrete heaps: let $h_0, h_1 \in (\mathbb{V}_{addr} \to \mathbb{V})$, such that $\operatorname{dom}(h_0) \cap \operatorname{dom}(h_1) = \emptyset$; then, we let $h_0 \otimes h_1$ be defined by:

$$h_0 \circledast h_1:$$
 $dom(h_0) \cup dom(h_1) \longrightarrow \mathbb{V}$
 $x \in dom(h_0) \longmapsto h_0(x)$
 $x \in dom(h_1) \longmapsto h_1(x)$

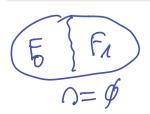
Separating conjunction

• Predicate:

$$F ::= \ldots \mid F_0 * F_1$$

Concretization:

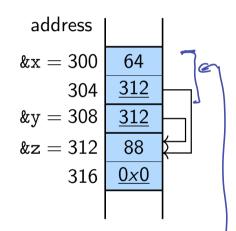
$$\gamma(\mathsf{F}_0 * \mathsf{F}_1) = \{ (e, h_0 \circledast h_1) \mid (e, h_0) \in \gamma(\mathsf{F}_0) \land (e, h_1) \in \gamma(\mathsf{F}_1) \}$$



An example

Concrete memory layout

(pointer values underlined)



 $\begin{array}{ccccc} e: & \mathtt{x} & & \mapsto & 300 \\ & \mathtt{y} & & \mapsto & 308 \\ & \mathtt{z} & & \mapsto & 312 \end{array}$

 $h: 300 \mapsto 64$ $304 \mapsto 312$ $308 \mapsto 312$ $312 \mapsto 88$ $316 \mapsto 0$

A formula that abstracts away the addresses:

$$\&x \mapsto \langle 64, \&z \rangle * \&y \mapsto \&z * \&z \mapsto \langle 88, 0 \rangle$$

&.OH64 * &x.4 HD &2 *

Separation logic: non separating conjunction

We can also add the **conventional conjunction operator**, with its **usual concretization**:

Non separating conjunction

• Predicate:

$$F ::= \ldots \mid F_0 \wedge F_1 \mid$$

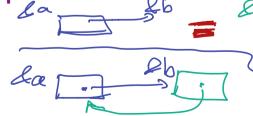
Concretization:

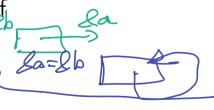
$$\gamma(\mathsf{F}_0 \wedge \mathsf{F}_1) = \gamma(\mathsf{F}_0) \cap \gamma(\mathsf{F}_1)$$

Exercise: describe and compare the concretizations of









Separating conjunction vs non separating conjunction

- Classical conjunction: properties for the same memory region
- Separating conjunction: properties for disjoint memory regions

&a
$$\mapsto$$
 &b \land &b \mapsto a

- the same heap verifies &a \mapsto &b and &b \mapsto &a
- there can be only one cell
- thus a = b

$$\&a \mapsto \&b * \&b \mapsto \&a$$

- two separate sub-heaps respectively satisfy &a → &b and &b → &a
- thus $a \neq b$
- Separating conjunction and non-separating conjunction have very different properties
- Both express very different properties e.g., no ambiguity on weak / strong updates

Separating and non separating conjunction

Logic rules of the two conjunction operators of SL:

Separating conjunction:

$$\frac{(e,h_0)\in\gamma(F_0)\quad (e,h_1)\in\gamma(F_1)}{(e,h_0\circledast h_1)\in\gamma(F_0*F_1)} \longrightarrow \underset{\text{congunction}}{\text{linear}}$$

Non separating conjunction:

$$\frac{(e,h)\in\gamma(\mathsf{F}_0)\quad (e,h)\in\gamma(\mathsf{F}_1)}{(e,h)\in\gamma(\mathsf{F}_0\wedge\mathsf{F}_1)} \longrightarrow \begin{array}{c} \text{additive} \\ \text{conyardion} \end{array}$$

Reminiscent of Linear Logic [Girard87]: resource aware / non resource aware conjunction operators

Separation logic: empty store

Empty store

• Predicate:

• Concretization:

$$\gamma(\mathsf{emp}) = \{(e,[]) \mid e \in \mathbb{E}\} = \mathbb{E} \times \{[]\}$$
 where [] denotes the empty store

- emp is the neutral element for *
 (monoid structure induced by *)
- by contrast the **neutral element for** \wedge is TRUE, with concretization:

$$\gamma(\mathtt{TRUE}) = \mathbb{E} imes \mathbb{H}$$

Separation logic: other connectors

Disjunction:

- $F ::= ... | F_0 \vee F_1 |$
- concretization:

$$\gamma(\mathsf{F}_0 \vee \mathsf{F}_1) = \gamma(\mathsf{F}_0) \underbrace{\cup \gamma(\mathsf{F}_1)}$$

Spatial implication (aka, magic wand):

- $F ::= ... | F_0 *F_1 |$
- concretization:

 very powerful connector to describe structure segments, used in complex SL proofs

Separation logic

Summary of the main separation logic constructions seen so far:

Separation logic main connectors

```
\begin{array}{lll} \gamma(\mathsf{emp}) &=& \mathbb{E} \times \{[]\} \\ \gamma(\mathsf{TRUE}) &=& \mathbb{E} \times \mathbb{H} \\ \gamma(\mathsf{1} \mapsto \mathsf{v}) &=& \{(e,[[\![\mathsf{1}]\!](e,\hbar) \mapsto \mathsf{v}]) \mid e \in \mathbb{E}\} \\ \gamma(\mathsf{F}_0 * \mathsf{F}_1) &=& \{(e,\hbar_0 \circledast h_1) \mid (e,\hbar_0) \in \gamma(\mathsf{F}_0) \land (e,h_1) \in \gamma(\mathsf{F}_1)\} \\ \gamma(\mathsf{F}_0 \land \mathsf{F}_1) &=& \gamma(\mathsf{F}_0) \cap \gamma(\mathsf{F}_1) \\ \gamma(\mathsf{F}_0 -\!\!\!\!*\; \mathsf{F}_1) &=& \{(e,\hbar) \mid \forall h_0 \in \mathbb{H}, \, (e,h_0) \in \gamma(\mathsf{F}_0) \Longrightarrow (e,\hbar \circledast h_0) \in \gamma(\mathsf{F}_1)\} \end{array}
```

Concretization of pure formulas is standard

How does this help for program reasoning?

Separation logic triple

Program proofs based on Hoare triples

• Notation: {F}p{F'} if and only if:

$$\forall s,s'\in\mathbb{S},\;s\in\gamma(\mathtt{F})\land s'\in\llbracket p
rbracket(s)\Longrightarrow s'\in\gamma(\mathtt{F}')$$

Application: formalize proofs of programs

A few rules (straightforward proofs):

$$F_0 \Longrightarrow F_0' \qquad \{F_0'\}b\{F_1'\} \qquad F_1' \Longrightarrow F_1 \qquad \text{consequence}$$

$$\{F_0\}b\{F_1\} \qquad \qquad \text{mutation}$$

$$\{\&x \mapsto ?\}x := e\{\&x \mapsto e\} \qquad \text{mutation-2}$$

$$x \text{ does not appear in } F \qquad \text{mutation-2}$$

$$\{\&x \mapsto ? * F\}x := e\{\&x \mapsto e * F\}$$

$$\text{mutation-2}$$

(we assume that e does not allocate memory)

The frame rule

What about the resemblance between rules "mutation" and "mutation-2"?

Theorem: the frame rule

$$\frac{\{F_0\}b\{F_1\}\quad \text{freevar}(F)\cap \text{write}(b)=\emptyset}{\{F_0*F\}b\{F_1*F\}} \text{ frame}$$

- Proof by induction on the logical rules on program statements, i.e., essentially a large case analysis (see biblio for a more complete set of rules)
- Rules are proved by case analysis on the program syntax

The frame rule allows to reason locally about programs

Application of the frame rule

A program with intermittent invariants, derived using the frame rule, since each step impacts a disjoint region:

```
int i;
int * x;
int * y;
{&i ↦? * &x ↦? * &y ↦?}
    x = &i;
{&i ↦? * &x ↦ &i * &y ↦?}
    y = &i;
{&i ↦? * &x ↦ &i * &y ↦ &i}
    *x = 42;
{&i ↦ 42 * &x ↦ &i * &y ↦ &i}
```

Many other program proofs done using separation logic e.g., verification of the Deutsch-Shorr-Waite algorithm (biblio)

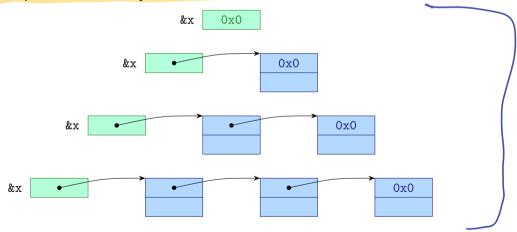
Summarization and inductive definitions

What do we still miss?

So far, formulas denote fixed sets of cells

Thus, no summarization of unbounded regions...

• Example all lists pointed to by x, such as:



 How to precisely abstract these stores with a single formula i.e., no infinite disjunction?

Inductive definitions in separation logic

List definition

$$\alpha \cdot \mathsf{list} \ := \ \alpha = 0 \land \mathsf{emp} \\ \lor \ \alpha \neq 0 \land \alpha \cdot \mathsf{next} \mapsto \delta * \alpha \cdot \mathsf{data} \mapsto \beta * \delta \cdot \mathsf{list}$$

• Formula abstracting our set of structures:

&x
$$\mapsto \alpha * \alpha \cdot \mathsf{list}$$

- Summarization: this formula is finite and describe infinitely many heaps
- Concretization: next slide...

Practical implementation in verification/analysis tools

- Verification: hand-written definitions
- Analysis: either built-in or user-supplied, or partly inferred

Concretization by unfolding

Intuitive semantics of inductive predicates

- Inductive predicates can be **unfolded**, by **unrolling their definitions**Syntactic unfolding is noted $\stackrel{\mathcal{U}}{\longrightarrow}$
- A formula F with inductive predicates describes all stores described by all formulas F' such that F $\stackrel{\mathcal{U}}{\longrightarrow}$ F'

Example:

• Let us start with $x \mapsto \alpha_0 * \alpha_0 \cdot \text{list}$; we can unfold it as follows:

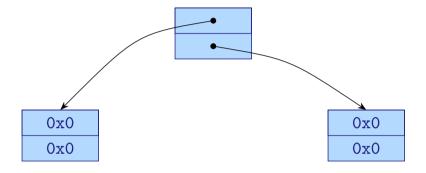
$$\begin{array}{ll} & & & \\ \frac{\mathcal{U}}{\longrightarrow} & & & \\ & & \\ \frac{\mathcal{U}}{\longrightarrow} & &$$

We get the concrete state below:



Example: tree

• Example:



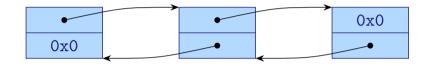
Inductive definition

• Two recursive calls instead of one:

$$\begin{array}{ll} \alpha \cdot \mathsf{tree} &:= & \alpha = \mathsf{0} \, \wedge \, \mathsf{emp} \\ & \vee & \alpha \neq \mathsf{0} \, \wedge \, \alpha \cdot \mathsf{left} \mapsto \beta * \alpha \cdot \mathsf{right} \mapsto \delta \\ & * \beta \cdot \mathsf{tree} * \delta \cdot \mathsf{tree} \end{array}$$

Example: doubly linked list

• Example:



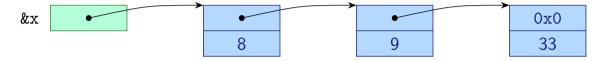
Inductive definition

• We need to propagate the prev pointer as an additional parameter:

$$\begin{array}{lll} \alpha \cdot \mathsf{dII}(\delta) &:= & \alpha = 0 \, \wedge \, \mathsf{emp} \\ & \vee & \alpha \neq 0 \, \wedge \, \alpha \cdot \mathsf{next} \mapsto \beta * \alpha \cdot \mathsf{prev} \mapsto \delta \\ & & * \beta \cdot \mathsf{dII}(\alpha) \end{array}$$

Example: sortedness

• Example: sorted list



Inductive definition

- Each element should be greater than the previous one
- The first element simply needs be greater than $-\infty...$
- We need to propagate the lower bound, using a scalar parameter

Outline

- 1 Memory models
- Pointer Abstractions
- Separation Logic
- 4 A shape abstract domain relying on separation
- 5 Standard static analysis algorithms
- 6 Conclusion
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Design of an abstract domain

A lot of things are missing to turn SL into an abstract domain

Set of logical predicates:

- separation logic formulas are very expressive
 e.g., arbitrary alternations of ∧ and *
- such expressiveness is not necessarily required in static analysis

Representation:

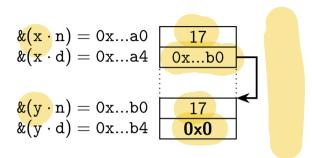
- unstructured formulas can be represented as ASTs,
 but this representation is not easy to manipulate efficiently
- intuition over memory states typically involves graphs

Analysis algorithms:

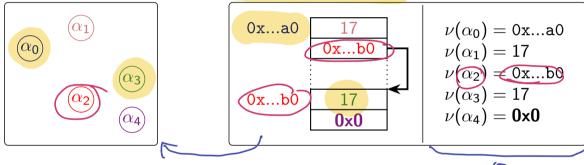
• inference of "optimal" invariants in SL, with numerical predicates obviously not computable

Concrete memory states

- very low level description numeric offsets / field names
- pointers, numeric values: raw sequences of bits

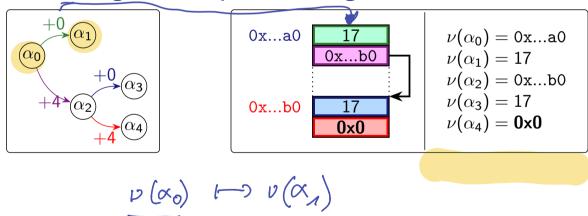


- Concrete memory states
- Abstraction of values into symbolic variables (nodes)



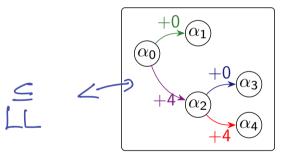
- characterized by valuation ν
- ightharpoonup
 u maps symbolic variables into concrete addresses

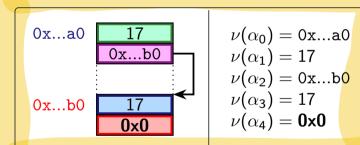
- Concrete memory states
- Abstraction of values into symbolic variables / nodes
- Abstraction of regions into points-to edges



- Concrete memory states
- Abstraction of values into symbolic variables / nodes

Abstraction of regions into points-to edges





Shape graph concretization

$$\gamma_{\mathsf{sh}}(\mathsf{G}) = \{(\mathsf{h}, \nu) \mid \ldots\}$$

valuation ν plays an important role to combine abstraction...

Structure of shape graphs

Valuations bridge the gap between nodes and values

Symbolic variables / nodes and intuitively abstract concrete values:

Symbolic variables

We let \mathbb{V}^{\sharp} denote a countable set of **symbolic variables**; we usually let them be denoted by Greek letters in the following: $\mathbb{V}^{\sharp} = \{\alpha, \beta, \delta, \ldots\}$

When concretizing a shape graph, we need to characterize how the concrete instance evaluates each symbolic variable, which is the purpose of the valuation functions:

Valuations

A valuation is a function from symbolic variables into concrete values (and is often denoted by ν): Val = $\mathbb{V}^{\sharp} \longrightarrow \mathbb{V}$

Note that valuations treat in the same way addresses and raw values

Structure of shape graphs

Distinct edges describe separate regions

In particular, if we **split** a graph into **two parts**:

Separating conjunction

$$\gamma_{\mathsf{sh}}(S_0^\sharp * S_1^\sharp) = \{(h_0 \circledast h_1, \nu) \mid (h_0, \nu) \in \gamma_{\mathsf{sh}}(S_0^\sharp) \land (h_1, \nu) \in \gamma_{\mathsf{sh}}(S_1^\sharp)\}$$

Similarly, when considering the **empty set of edges**, we get the empty heap (where V^{\sharp} is the set of nodes):

$$\gamma_{\mathsf{sh}}(\mathsf{emp}) = \{(\emptyset, \nu) \mid \nu : \mathbb{V}^{\sharp} \to \mathbb{V}\}$$

Abstraction of contiguous regions

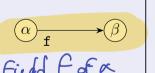
A single points-to edge represents one heap cell

A points-to edge encodes basic points to predicate in separation logic:

Points-to edges

Syntax

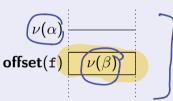
Graph edge



Separation logic formula

$$\alpha \cdot \mathtt{f} \mapsto \beta$$

Concrete view



$$\gamma_{\mathsf{sh}}(\alpha \cdot \mathtt{f} \mapsto \beta) = \{([\nu(\alpha) + \mathsf{offset}(\mathtt{f}) \mapsto \nu(\beta)], \nu) \mid \nu : \{\alpha, \beta, \ldots\} \to \mathbb{N}\}$$

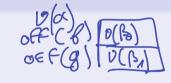
Abstraction of contiguous regions

Contiguous regions are described by adjacent points-to edges

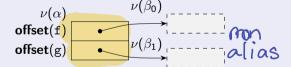
To describe **blocks** containing series of **cells** (*e.g.*, in a **C** structure), shape graphs utilize several outgoing edges from the node representing the base address of the block

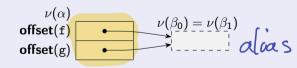
Field splitting model

Separation impacts edges / fields, not pointers



• Shape graph accounts for both abstract states below:



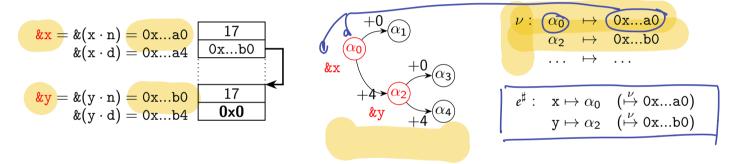


In other words, in a field splitting model, separation:

- asserts addresses are distinct
- says nothing about contents

Abstraction of the environment

Environments bind variables to their (concrete / abstract) address

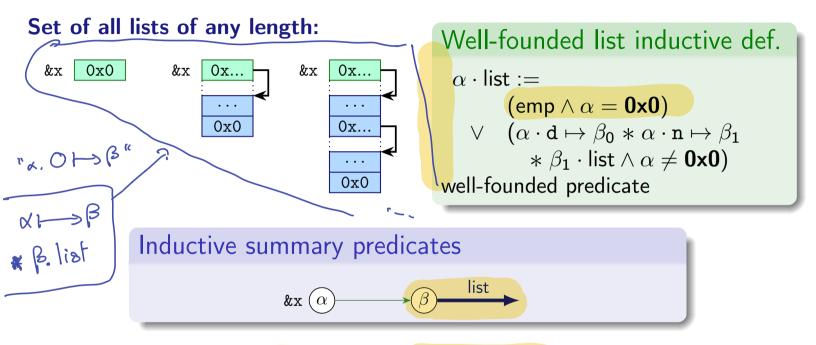


Abstract environments

- An abstract environment is a function e^{\sharp} from variables to symbolic nodes
- The concretization extends as follows:

$$\gamma_{\mathsf{mem}}(e^{\sharp},S^{\sharp}) = \{(e,h,
u) \mid (h,
u) \in \gamma_{\mathsf{sh}}(S^{\sharp}) \land e =
u \circ e^{\sharp}\}$$

Basic abstraction: summarization



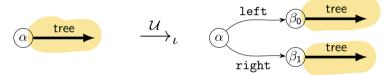
Concretization based on unfolding and least-fixpoint:

- $\stackrel{\mathcal{U}}{\longrightarrow}$ replaces an α · list predicate with one of its premises
- $\gamma(S^{\sharp}, F) = \bigcup \{ \gamma(S_{u}^{\sharp}, F_{u}) \mid (S^{\sharp}, F) \xrightarrow{\mathcal{U}} (S_{u}^{\sharp}, F_{u}) \}$

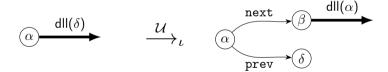
Inductive structures: a few instances

As before, **many interesting inductive predicates** encode nicely into graph inductive definitions:

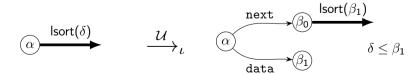
More complex shapes: trees



Relations among pointers: doubly-linked lists



• Relations between pointers and numerical: sorted lists



Inductive segments

A frequent pattern:

- A first attempt:
 - x points to a list, so $\&x \mapsto \alpha * \alpha \cdot \text{list holds}$
 - y points to a list, so &y $\mapsto \beta * \beta \cdot \text{list}$ holds

However, the following does not hold

$$\&x \mapsto \alpha * \alpha \cdot \mathsf{list} * \&y \mapsto \beta * \beta \cdot \mathsf{list}$$

Why ? violation of separation!

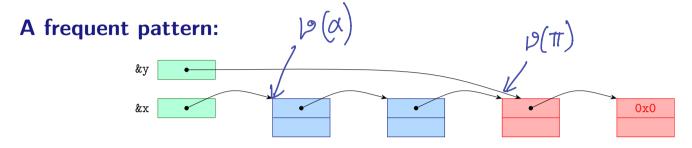
A second attempt:

$$(\&x \mapsto \alpha * \alpha \cdot list * TRUE) \land (\&y \mapsto \beta * \beta \cdot list * TRUE)$$

Why is it still not all that good ? relation lost!

0x0

Inductive segments

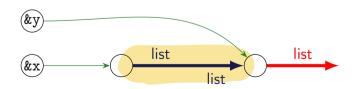


Could be **expressed directly** as an inductive with a parameter:

$$lpha \cdot \mathsf{list_endp}(\pi) ::= (emp, lpha = \pi) \ (lpha \cdot \mathsf{next} \mapsto eta_0 * lpha \cdot \mathsf{data} \mapsto eta_1 \ * eta_0 \cdot \mathsf{list_endp}(\pi), lpha \neq 0)$$

This definition straightforwardly derives from list

Thus, we make segments part of the fundamental predicates of the domain



Multi-segments: possible, but harder for analysis

Shape graphs and separation logic

Semantic preserving translation Π of graphs into separation logic formulas:

Graph $S^\sharp \in \mathbb{D}^\sharp_{sh}$	Translated formula $\Pi(S^{\sharp})$
$ \overbrace{\alpha} \xrightarrow{\mathbf{f}} $	$\alpha \cdot \mathtt{f} \mapsto \beta$
S_0^{\sharp} S_1^{\sharp}	$\Pi(S_0^\sharp) * \Pi(S_1^\sharp)$
(a) list	$lpha \cdot list$
$ \begin{array}{c c} \hline \alpha & \text{list} \\ \hline \delta \end{array} $	$lpha \cdot list_endp(\delta)$
other inductives and segments	similar

Note that:

- shape graphs can be encoded into separation logic formula
- the opposite is usually not true

Value information:

- discussed in the next course
- intuitively, assume we maintain numerical information next to shape graphs

Outline

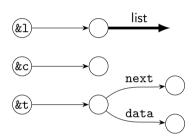
- Memory models
- 2 Pointer Abstractions
- Separation Logic
- 4 A shape abstract domain relying on separation
- 5 Standard static analysis algorithms
 - Overview of the analysis
 - Post-conditions and unfolding
 - Folding: widening and inclusion checking
 - Abstract interpretation framework: assumptions and results
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Static analysis overview

A list insertion function:

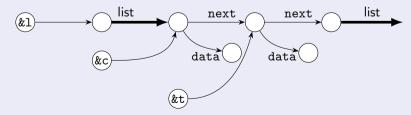
```
list * 1 assumed to point to a list
list * t assumed to point to a list element
list * c = 1;
while(c != NULL && c -> next != NULL && (...)){
    c = c -> next;
}
t -> next = c -> next;
c -> next = t;
```

- list inductive structure def.
- Abstract precondition:



Result of the (interprocedural) analysis

• Over-approximations of reachable concrete states *e.g.*, after the insertion:



Transfer functions

Abstract interpreter design

- Follows the semantics of the language under consideration
- The abstract domain should provide sound transfer functions

Transfer functions:

- Assignment: $x \to f = y \to g$ or $x \to f = e_{arith}$
- Test: analysis of conditions (if, while)
- Variable creation and removal
- Memory management: malloc, free

Abstract operators:

- Join and widening: over-approximation
- Inclusion checking: check stabilization of abstract iterates

Should be sound i.e., not forget any concrete behavior

Abstract operations

Denotational style abstract interpreter

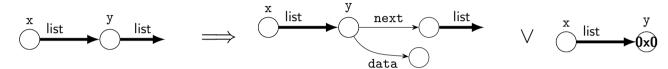
- Concrete denotational semantics $\llbracket b \rrbracket : \mathbb{S} \longrightarrow \mathcal{P}(\mathbb{S})$
- Abstract post-condition [b] *(S), computed by the analysis:

$$s \in \gamma(S) \Longrightarrow \llbracket b \rrbracket(s) \subseteq \gamma(\llbracket b \rrbracket^{\sharp}(S))$$

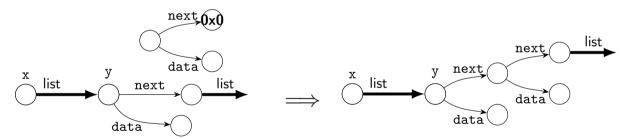
Analysis by induction on the syntax using domain operators

The algorithms underlying the transfer functions

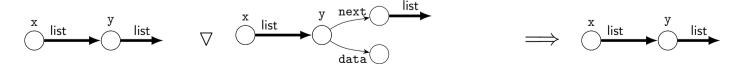
Unfolding: cases analysis on summaries



• Abstract postconditions, on "exact" regions, e.g. insertion



Widening: builds summaries and ensures termination



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Analysis of an assignment in the graph domain

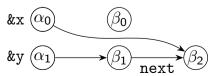
Steps for analyzing $x = y \rightarrow next$ (local reasoning)

- **1** Evaluate **I-value** x into **points-to edge** $\alpha \mapsto \beta$
- 2 Evaluate **r-value** y -> next into **node** β'
- **3** Replace points-to edge $\alpha \mapsto \beta$ with **points-to edge** $\alpha \mapsto \beta'$

With pre-condition:

&x
$$(\alpha_0)$$
 (β_0) &y (α_1) (β_1) (α_1) (β_2)

- Step 1 produces $\alpha_0 \mapsto \beta_0$
- Step 2 produces β_2
- End result:



With pre-condition:

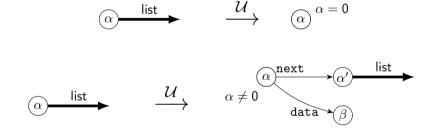
&x
$$\alpha_0$$
 β_0 β_1 list

- Step 1 produces $\alpha_0 \mapsto \beta_0$
- Step 2 fails
- Abstract state too abstract
- We need to refine it

Unfolding as a local case analysis

Unfolding principle

- Case analysis, based on the inductive definition
- Generates symbolic disjunctions (analysis performed in a disjunction domain, e.g., trace partitioning)
- Example, for lists:



• Numeric predicates: next course on shape + value abstraction

Soundness: by definition of the concretization of inductive structures

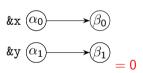
$$\gamma_{\mathsf{sh}}(S^\sharp) \subseteq \bigcup \{\gamma_{\mathsf{sh}}(S_0^\sharp) \mid S^\sharp \stackrel{\mathcal{U}}{\longrightarrow} S_0^\sharp \}$$

Analysis of an assignment, with unfolding

Principle

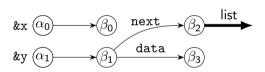
- We have $\gamma_{\sf sh}(\alpha \cdot \iota) = \bigcup \{ \gamma_{\sf sh}(S^{\sharp}) \mid \alpha \cdot \iota \xrightarrow{\mathcal{U}} S^{\sharp} \}$
- Replace $\alpha \cdot \iota$ with a finite number of disjuncts and continue

Disjunct 1:

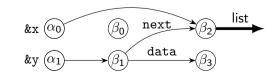


- Step 1 produces $\alpha_0 \mapsto \beta_0$
- Step 2 fails: Null pointer!
- In a **correct** program, would be ruled out by a **condition** $y \neq 0$ *i.e.*, $\beta_1 \neq 0$ in $\mathbb{D}_{\text{num}}^{\sharp}$

Disjunct 2:



- Step 1 produces $\alpha_0 \mapsto \beta_0$
- Step 2 produces β_2
- End result:



Unfolding and degenerated cases

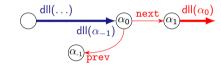
assume(1 points to a dl1) c = 1; ① while($c \neq NULL \&\& condition$) $c = c \rightarrow next$; ② if($c \neq 0 \&\& c \rightarrow prev \neq 0$) $c = c \rightarrow prev \rightarrow prev$;

• at 1: $\textcircled{a_0} \xrightarrow{\operatorname{dll}(\delta_1)}$

• at 2: $\frac{\operatorname{dil}(\delta_0)}{\operatorname{1}}$ $\frac{\operatorname{dil}(\delta_1)}{\operatorname{c}}$

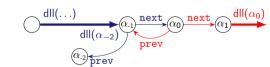
 \Rightarrow non trivial unfolding

• Materialization of c -> prev:



Segment splitting lemma: basis for segment unfolding

• Materialization of c -> prev -> prev:



 Implementation issue: discover which inductive edge to unfold very hard!

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Need for a folding operation

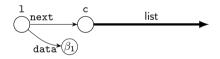
Back to the **list traversal** example:

First iterates in the loop:

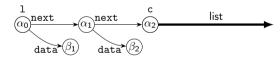
• at **iteration 0** (before entering the loop):

$$0 \qquad \qquad \text{list}$$

• at iteration 1:



• at iteration 2:



```
\begin{aligned} & \textbf{assume}(\texttt{l points to a list}) \\ & c = \texttt{l}; \\ & \textbf{while}(\texttt{c} \neq \texttt{NULL}) \{ \\ & c = \texttt{c} \rightarrow \texttt{next}; \\ & \} \end{aligned}
```

The analysis **unfolds**, but **never folds**:

$$\begin{array}{c}
S_0 \\
\text{unfold} \\
S_{0,u} \xrightarrow{f} S_1 \\
\text{unfold} \\
S_{1,u} \xrightarrow{f} S_2 \cdots
\end{array}$$

- How to guarantee termination of the analysis?
- How to introduce segment edges / perform abstraction ?

Widening

- The lattice of shape abstract values has infinite height
- Thus iteration sequences may not terminate

Over-approximates join:

$$\left\{ \begin{array}{l} \gamma(X^{\sharp}) & \subseteq & \gamma(X^{\sharp} \vee Y^{\sharp}) \\ \gamma(Y^{\sharp}) & \subseteq & \gamma(X^{\sharp} \vee Y^{\sharp}) \end{array} \right.$$

• Enforces termination: for all sequence $(X_n^{\sharp})_{n\in\mathbb{N}}$, the sequence $(Y_n^{\sharp})_{n\in\mathbb{N}}$ defined below is ultimately stationary

$$\left\{ \begin{array}{ccc} Y_0^{\sharp} & = & X_0^{\sharp} \\ \forall n \in \mathbb{N}, \ Y_{n+1}^{\sharp} & = & Y_n^{\sharp} \ \nabla X_{n+1}^{\sharp} \end{array} \right.$$

Canonicalization

Upper closure operator

 $\rho: \mathbb{D}^{\sharp} \longrightarrow \mathbb{D}_{can}^{\sharp} \subseteq \mathbb{D}^{\sharp}$ is an **upper closure operator** (uco) iff it is monotone, extensive and idempotent.

Canonicalization

- Disjunctive completion: $\mathbb{D}^{\sharp}_{\vee}$ = finite disjunctions over \mathbb{D}^{\sharp}
- Canonicalization operator ρ_{\vee} defined by $\rho_{\vee}: \mathbb{D}^{\sharp}_{\vee} \longrightarrow \mathbb{D}^{\sharp}_{can_{\vee}}$ and $\rho_{\vee}(X^{\sharp}) = \{\rho(x^{\sharp}) \mid x^{\sharp} \in X^{\sharp}\}$ where ρ is an uco and $\mathbb{D}_{can}^{\sharp}$ is finite
- Canonicalization is used in many shape analysis tools
- Easier to compute but less powerful than widening: does not exploit history of computation

Weakening: definition

To design **inclusion test**, **join** and **widening** algorithms, we first study a more general notion of **weakening**:

Weakening

We say that S_0^{\sharp} can be weakened into S_1^{\sharp} if and only if

$$\forall (\hbar, \nu) \in \gamma_{\mathsf{sh}}(S_0^\sharp), \; \exists \nu' \in \mathsf{Val}, \; (\hbar, \nu') \in \gamma_{\mathsf{sh}}(S_1^\sharp)$$

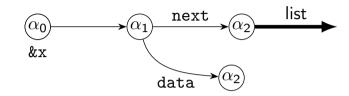
We then note $S_0^{\sharp} \preccurlyeq S_1^{\sharp}$

Applications:

- inclusion test (comparison) inputs $S_0^{\sharp}, S_1^{\sharp}$; if returns true $S_0^{\sharp} \preccurlyeq S_1^{\sharp}$
- canonicalization (unary weakening) inputs S_0^{\sharp} and returns $\rho(S_0^{\sharp})$ such that $S_0^{\sharp} \preccurlyeq \rho(S_0^{\sharp})$
- widening / join (binary weakening ensuring termination or not) inputs $S_0^{\sharp}, S_1^{\sharp}$ and returns S_{up}^{\sharp} such that $S_i^{\sharp} \preccurlyeq S_{\mathrm{up}}^{\sharp}$

Weakening: example

We consider S_0^{\sharp} defined by:



and S_1^{\sharp} defined by:

$$\beta_0$$
 list α

Then, we have the weakening $S_0^{\sharp} \leq S_1^{\sharp}$ up-to a renaming in S_1^{\sharp} :

$$\Psi: \beta_0 \longmapsto \alpha$$

- weakening up-to renaming is generally required as graphs do not have the same name space
- formalized a bit later...

Local weakening: separating conjunction rule

We can apply the local reasoning principle to weakening

If $S_0^{\sharp} \preccurlyeq S_{0,\text{weak}}^{\sharp}$ and $S_1^{\sharp} \preccurlyeq S_{1,\text{weak}}^{\sharp}$ then:

$$\alpha_0$$
 S_0^{\sharp} α_1 S_1^{\sharp} α_2 \prec α_0 $S_{0,\text{weak}}^{\sharp}$ α_1 $S_{1,\text{weak}}^{\sharp}$ α_2

Separating conjunction rule (\preccurlyeq_*)

Let us assume that

- S_0^{\sharp} and S_1^{\sharp} have distinct set of source nodes
- we can weaken S_0^{\sharp} into $S_{0,\text{weak}}^{\sharp}$
- we can weaken S_1^{\sharp} into $S_{1,\text{weak}}^{\sharp}$

then:

we can weaken
$$S_0^{\sharp} * S_1^{\sharp}$$
 into $S_{0,\text{weak}}^{\sharp} * S_{1,\text{weak}}^{\sharp}$

Local weakening: unfolding rule, identity rule

Weakening unfolded region $(\leq_{\mathcal{U}})$

Let us assume that $S_0^{\sharp} \xrightarrow{\mathcal{U}} S_1^{\sharp}$. Then, by definition of the concretization of unfolding

we can weaken
$$S_1^{\sharp}$$
 into S_0^{\sharp}

- the proof follows from the definition of unfolding
- it can be applied locally, on graph regions that differ due to unfolding of inductive definitions

Identity weakening (\preccurlyeq_{Id})

we can weaken S^{\sharp} into S^{\sharp}

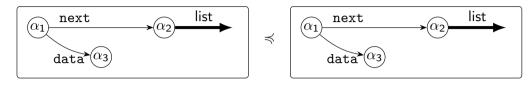
• the proof is trivial:

$$\gamma_{\mathsf{sh}}(S^\sharp) \subseteq \gamma_{\mathsf{sh}}(S^\sharp)$$

 on itself, this principle is not very useful, but it can be applied locally, and combined with $(\leq_{\mathcal{U}})$ on graph regions that are not equal

Local weakening: example

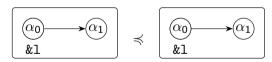
By rule (\preccurlyeq_{Id}):



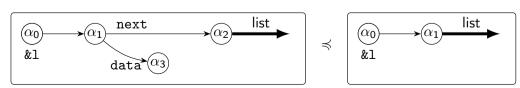
Thus, by **rule** ($\preccurlyeq_{\mathcal{U}}$):



Additionally, by **rule** (\preccurlyeq_{Id}):



Thus, by **rule** (\preccurlyeq_*):



Inclusion checking rules in the shape domain

Graphs to compare have distinct sets of nodes, thus inclusion check should carry out a **valuation transformer** $\Psi: \mathbb{V}^{\sharp}(S_{1}^{\sharp}) \longrightarrow \mathbb{V}^{\sharp}(S_{0}^{\sharp})$ (important when dealing also with content values)

Using (and extending) the weakening principles, we obtain the following rules (considering only inductive definition list, though these rules would extend to other definitions straightforwardly):

• Identity rules:

$$\forall i, \ \Psi(\beta_i) = \alpha_i \implies \alpha_0 \cdot \mathbf{f} \mapsto \alpha_1 \quad \sqsubseteq^{\sharp}_{\Psi} \quad \beta_0 \cdot \mathbf{f} \mapsto \beta_1$$

$$\Psi(\beta) = \alpha \quad \Longrightarrow \quad \alpha \cdot \mathsf{list} \quad \sqsubseteq^{\sharp}_{\Psi} \quad \beta \cdot \mathsf{list}$$

$$\forall i, \ \Psi(\beta_i) = \alpha_i \quad \Longrightarrow \quad \alpha_0 \cdot \mathsf{list_endp}(\alpha_1) \quad \sqsubseteq^{\sharp}_{\Psi} \quad \beta_0 \cdot \mathsf{list_endp}(\beta_1)$$

• Rules on inductives:

$$\forall i,\ \Psi(\beta_i) = \alpha \quad \Longrightarrow \quad \mathsf{emp} \quad \sqsubseteq^\sharp_\Psi \quad \beta_0 \cdot \mathsf{list_endp}(\beta_1)$$

$$S_0^\sharp \sqsubseteq^\sharp_\Psi S_1^\sharp \wedge \beta \cdot \iota \stackrel{\mathcal{U}}{\longrightarrow} S_1^\sharp \quad \Longrightarrow \quad S_0^\sharp \quad \sqsubseteq^\sharp_\Psi \quad \beta \cdot \iota$$
 if β_1 fresh $,\Psi' = \Psi[\beta_1 \mapsto \alpha_1]$ and $\Psi(\beta_0) = \alpha_0$ then,
$$S_0^\sharp \sqsubseteq^\sharp_{\Psi'} \beta_1 \cdot \mathsf{list} \quad \Longrightarrow \quad \alpha_0 \cdot \mathsf{list_endp}(\alpha_1) * S_0^\sharp \quad \sqsubseteq^\sharp_\Psi \quad \beta_0 \cdot \iota$$

Inclusion checking algorithm

Comparison of $(e_0^{\sharp}, S_0^{\sharp})$ and $(e_1^{\sharp}, S_1^{\sharp})$

- start with Ψ defined by $\Psi(\beta) = \alpha$ if and only if there exists a variable x such that $e_0^{\sharp}(\mathbf{x}) = \alpha \wedge e_1^{\sharp}(\mathbf{x}) = \beta$
- $\mathbf{2}$ iteratively **apply local rules**, and extend $\mathbf{\Psi}$ when needed
- 3 return true when both shape graphs become empty
 - the first step ensures both environments are consistent

This algorithm is sound:

Soundness

$$(e_0^\sharp,S_0^\sharp)\sqsubseteq^\sharp(e_1^\sharp,S_1^\sharp) \implies \gamma(e_0^\sharp,S_0^\sharp)\subseteq \gamma(e_1^\sharp,S_1^\sharp)$$

Over-approximation of union

The principle of join and widening algorithm is similar to that of \Box^{\sharp} :

• It can be computed **region by region**, as for weakening in general: If $\forall i \in \{0,1\}, \ \forall s \in \{\text{lft}, \text{rgh}\}, \ S_{i,s}^{\sharp} \preccurlyeq S_{s}^{\sharp}$,



The partitioning of inputs / different nodes sets requires a **node correspondence function**

$$\Psi: \mathbb{V}^\sharp(S^\sharp_{\mathrm{lft}}) imes \mathbb{V}^\sharp(S^\sharp_{\mathrm{rgh}}) \longrightarrow \mathbb{V}^\sharp(S^\sharp)$$

 The computation of the shape join progresses by the application of local join rules, that produce a new (output) shape graph, that weakens both inputs

Over-approximation of union: syntactic identity rules

In the next few slides, we focus on ∇ though the abstract union would be defined similarly in the shape domain

Several rules derive **from** (\preccurlyeq_{Id}):

• If $S_{\mathrm{lft}}^{\sharp} = \alpha_0 \cdot \mathbf{f} \mapsto \alpha_1$ and $S_{\mathrm{lft}}^{\sharp} = \beta_0 \cdot \mathbf{f} \mapsto \beta_1$ and $\Psi(\alpha_0, \beta_0) = \delta_0$, $\Psi(\alpha_1, \beta_1) = \delta_1$, then:

$$S_{ ext{lft}}^{\sharp} igtriangledown S_{ ext{rgh}}^{\sharp} = \delta_0 \cdot \mathtt{f} \mapsto \delta_1$$

• If $S_{\mathrm{lft}}^{\sharp} = \alpha_0 \cdot \mathrm{list}$ and $S_{\mathrm{lft}}^{\sharp} = \beta_0 \cdot \mathrm{list}_1$ and $\Psi(\alpha_0, \beta_0) = \delta_0$, then:

$$S^{\sharp}_{
m lft} lackbrachta S^{\sharp}_{
m rgh} = \delta_0 \cdot {\sf list}$$

Over-approximation of union: segment introduction rule

Rule

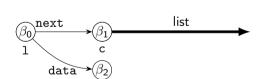


Application to list traversal, at the end of iteration 1:

before iteration 0:



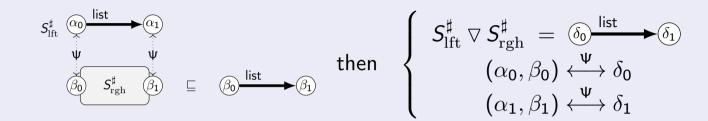
• end of iteration 0:



• join, before iteration 1:

Over-approximation of union: segment extension rule

Rule

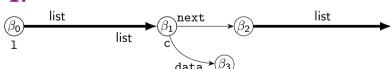


Application to list traversal, at the end of iteration 1:

• previous invariant before iteration 1:

$$\begin{array}{c|c}
\alpha_0 & \text{list} & & \alpha_1 \\
\hline
& & \text{list} & & c
\end{array}$$

• end of iteration 1:



• join, before iteration 1:

Over-approximation of union: rewrite system properties

- Comparison, canonicalization and widening algorithms can be considered rewriting systems over tuples of graphs
- Success configuration: weakening applies on all components, *i.e.*, the inputs are fully "consumed" in the weakening process
- Failure configuration: some components cannot be weakened i.e., the algorithm should return the conservative answer (i.e., \top)

Termination

- The systems are terminating
- This ensures comparison, canonicalization, widening are computable

Non confluence!

- The results depends on the order of application of the rules
- Implementation requires the choice of an adequate strategy

Over-approximation of union in the combined domain

Widening of $(e_0^{\sharp}, S_0^{\sharp})$ and $(e_1^{\sharp}, S_1^{\sharp})$

- define Ψ , e by $\Psi(\alpha, \beta) = e(\mathbf{x}) = \delta$ (where δ is a fresh node) if and only if $e_0^{\sharp}(\mathbf{x}) = \alpha \wedge e_1^{\sharp}(\mathbf{x}) = \beta$
- 2 iteratively apply join local rules, and extend Ψ when new relations are inferred (for instance for points-to edges)
- eturn the result obtained when all regions of both inputs are approximated in the output graph

This algorithm is sound:

Soundness

$$\gamma(e_0^{\sharp}, S_0^{\sharp}) \cup \gamma(e_1^{\sharp}, S_1^{\sharp}) \subseteq \gamma(e^{\sharp}, S^{\sharp})$$

Widening also enforces **termination** (it only introduces segments, and the growth induced by the introduction of segments is bounded)

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What assumptions do we make? How do we prove soundness of the analysis of a loop?

• Assumptions in the concrete level, and for block b:

$$(\mathcal{P}(\mathbb{M}),\subseteq)$$
 is a complete lattice, hence a CPO $F:\mathcal{P}(\mathbb{M})\to\mathcal{P}(\mathbb{M})$ is the concrete semantic ("post") function of b thus, the concrete semantics writes down as $[\![b]\!]=\mathsf{lfp}_\emptyset F$

Assumptions in the abstract level:

$$\mathbb{M}^{\sharp} \quad \text{set of abstract elements, no order a priori} \\ m^{\sharp} ::= (e^{\sharp}, S^{\sharp}) \\ \gamma_{\text{mem}} : \mathbb{M}^{\sharp} \to \mathcal{P}(\mathbb{M}) \quad \text{concretization} \\ F^{\sharp} : \mathbb{M}^{\sharp} \to \mathbb{M}^{\sharp} \quad \text{sound abstract semantic function} \\ i.e., \text{ such that } F \circ \gamma_{\text{mem}} \subseteq \gamma_{\text{mem}} \circ F^{\sharp} \\ \nabla : \mathbb{M}^{\sharp} \times \mathbb{M}^{\sharp} \to \mathbb{M}^{\sharp} \quad \text{widening operator, terminates, and such that} \\ \gamma_{\text{mem}}(m_{0}^{\sharp}) \cup \gamma_{\text{mem}}(m_{1}^{\sharp}) \subseteq \gamma_{\text{mem}}(m_{0}^{\sharp} \vee m_{1}^{\sharp}) \\ \end{cases}$$

Computing a loop abstract post-condition

Loop abstract semantics

The abstract semantics of loop **while**(rand()){b} is calculated as the limit of the sequence of abstract iterates below:

$$\begin{cases}
 m_0^{\sharp} = \bot \\
 m_{n+1}^{\sharp} = m_n^{\sharp} \nabla F^{\sharp}(m_n^{\sharp})
\end{cases}$$

Soundness proof:

- by induction over n, $\bigcup_{k \le n} F^k(\emptyset) \subseteq \gamma_{\text{mem}}(m_n^{\sharp})$
- by the property of widening, the abstract sequence converges at a rank N: $\forall k \geq N, \ m_k^{\sharp} = m_N^{\sharp}$, thus

$$\mathsf{lfp}_\emptyset F = \bigcup_k F^k(\emptyset) \subseteq \gamma_{\mathrm{mem}}(\mathit{m}_N^\sharp)$$

Discussion on the abstract ordering

How about the abstract ordering? We assumed NONE so far...

• Logical ordering, induced by concretization, used for proofs

$$m_0^{\sharp} \sqsubseteq m_1^{\sharp} \quad ::= \quad "\gamma_{\mathrm{mem}}(m_0^{\sharp}) \subseteq \gamma_{\mathrm{mem}}(m_1^{\sharp})"$$

 Approximation of the logical ordering, implemented as a function is le : $\mathbb{M}^{\sharp} \times \mathbb{M}^{\sharp} \to \{\text{true}, \top\}$, used to test the convergence of abstract iterates

$$\mathsf{is}_{\mathsf{le}}(\mathit{m}_{0}^{\sharp}, \mathit{m}_{1}^{\sharp}) = \mathsf{true} \quad \Longrightarrow \quad \gamma_{\mathrm{mem}}(\mathit{m}_{0}^{\sharp}) \subseteq \gamma_{\mathrm{mem}}(\mathit{m}_{1}^{\sharp})$$

Abstract semantics is not assumed (and is actually most likely NOT) monotone with respect to either of these orders...

 Also, computational ordering would be used for proving widening termination

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Updates and summarization

Weak updates cause significant precision loss... Separation logic makes updates strong

Separation logic

Separating conjunction combines properties on disjoint stores

- Fundamental idea: * forces to identify what is modified
- Before an update (or a read) takes place, memory cells need to be materialized
- Local reasoning: properties on unmodified cells pertain

Summaries

Inductive predicates describe unbounded memory regions

• Last lecture: array segments and transitive closure (TVLA)

Bibliography



- [JR]: Separation Logic: A Logic for Shared Mutable Data Structures. John C. Reynolds. In LICS'02, pages 55–74, 2002.
- [DHY]: A Local Shape Analysis Based on Separation Logic. Dino Distefano, Peter W. O'Hearn and Hongseok Yang. In TACAS'06, pages 287–302.
- [CR]: Relational inductive shape analysis. Bor-Yuh Evan Chang and Xavier Rival. In POPL'08, pages 247–260, 2008.

Assignment and paper reading

The Frame rule:

- formalize the Hoare logic rules for a language with pointer assignments and condition tests
- prove the Frame rule by induction over the syntax of programs

Reading:

Separation Logic: A Logic for Shared Mutable Data Structures. John C. Reynolds.

In LICS'02, pages 55-74, 2002.

Formalizes the Frame rule, among others

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Internships on memory abstraction

Several topics are possible, for instance:

Summarization based on universal quantification:

- memory abstractions use summaries today, we consider inductive linked structures; we will also see arrays...
- another form of summarization based on an unbounded set E

$$*{P(x) | x \in E}$$

requires the definition of fold / unfold, analysis operations...

- towards a parametric abstract domain:
 - generic dictionary abstraction
 - arrays (generalization of existing)
 - union finds and DAGs